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| Application Number | 09/667,164 |
| Filing Date | September 21, 2000 |
| First Named Inventor | William E. Ballachino |
| Art Unit | 2124 |
| Examiner Name | Chat C. Do |
| Attorney Docket Number | 00-C-050 |

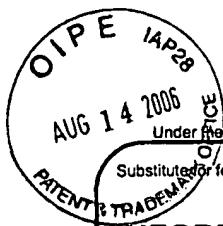
| FOREIGN PATENT DOCUMENTS | | | | | | |
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| Examiner Initials* | Cite No. ¹ | Foreign Patent Document | - | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear |
| | | Country Code ³ "Number ⁴ "Kind Code ⁵ (if known) | - | | | |
| CD | AC | EP 0 352 549 A2 | | 01/31/1990 | Siemens Aktiengesellschaft | |
| CD | AD | WO 98/51013 A1 | | 11/12/1998 | Xilinx, Inc. | |
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| CD | BA | Department of Electrical Engineering of Linkopings University, "Arithmetic Building Blocks", Prentice Hall, 1995, pp. 1-21, retrieved from the internet: www.ek.isy.liu.se/course/tsek30/current/Arithm.pdf , on June 1, 2006. | | |
| CD | BB | David A. Patterson et al., "Computer Architecture A Quantitative Approach", 1990 Morgan Kaufmann Publishers, Inc., San Francisco, California, pp. A45-A46. | | |
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